



US 20090237334A1

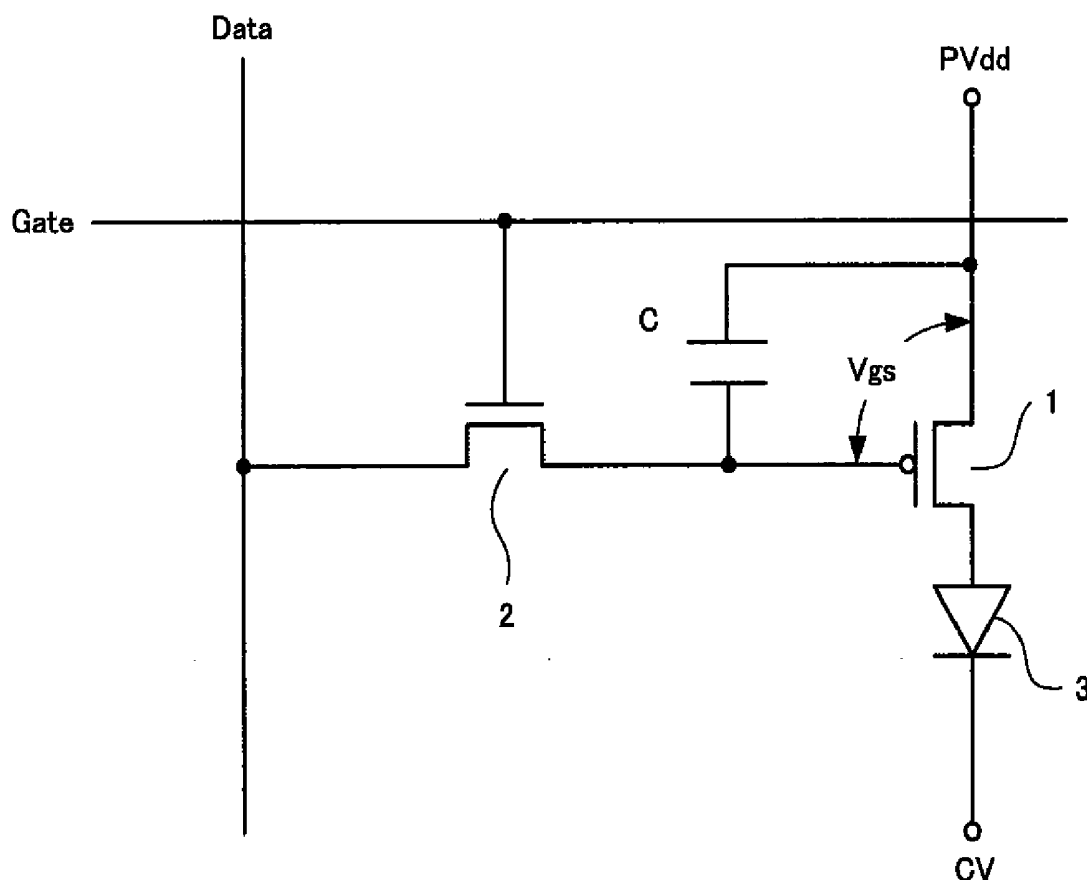
(19) **United States**(12) **Patent Application Publication**
Mizukoshi et al.(10) **Pub. No.: US 2009/0237334 A1**(43) **Pub. Date: Sep. 24, 2009**(54) **CORRECTING BRIGHTNESS VARIATIONS
IN ORGANIC ELECTROLUMINESCENT
PANEL****Publication Classification**(51) **Int. Cl.**
G09G 3/30 (2006.01)(52) **U.S. Cl.** **345/77; 345/80**(76) **Inventors:** **Seiichi Mizukoshi**, Kanagawa (JP);
Makoto Kohno, Kanagawa (JP);
Kouichi Onomura, Yokohama-shi
(JP)(57) **ABSTRACT**

Correspondence Address:
Raymond L. Owens, Patent Legal Staff
Eastman Kodak Company
343 State Street
Rochester, NY 14650-2201 (US)

An OLED display having a correction circuit for producing corrected image data in response to the first image data and in response to correction data to correct for brightness unevenness due to TFT variations; a memory for storing first image data or correction data; a switch effective in first and second states in response to a function switching signal having first and second conditions, respectively; and circuitry for causing the switch to be in the first state to connect the memory to the image input signal interface and to provide the stored first image data to the panel as the second image data; and for causing the switch to be in the second state to connect the memory to the correction circuit, provide the stored correction data to the correction circuit, and provide the corrected image data to the panel as the second image data.

(21) **Appl. No.:** **12/401,126**(22) **Filed:** **Mar. 10, 2009**(30) **Foreign Application Priority Data**

Mar. 18, 2008 (JP) 2008-068632



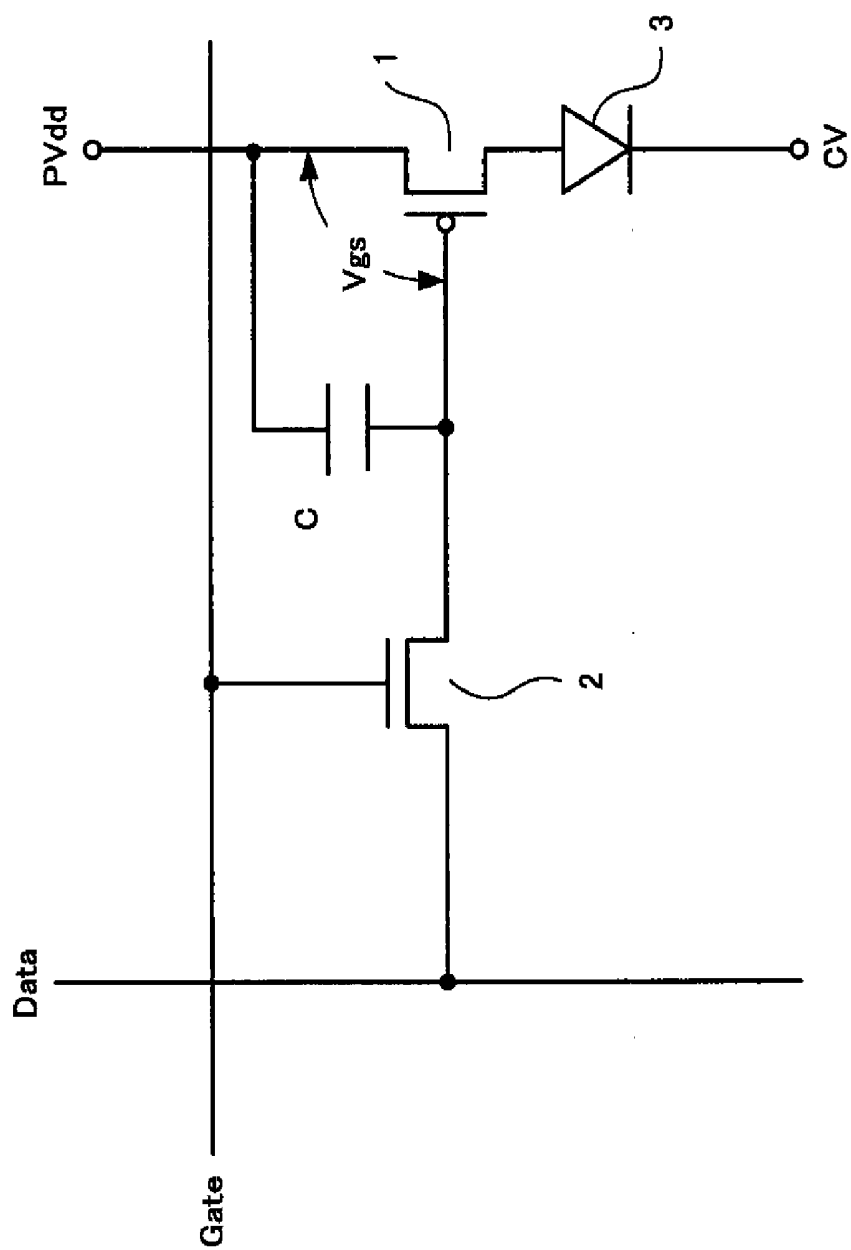


FIG. 1

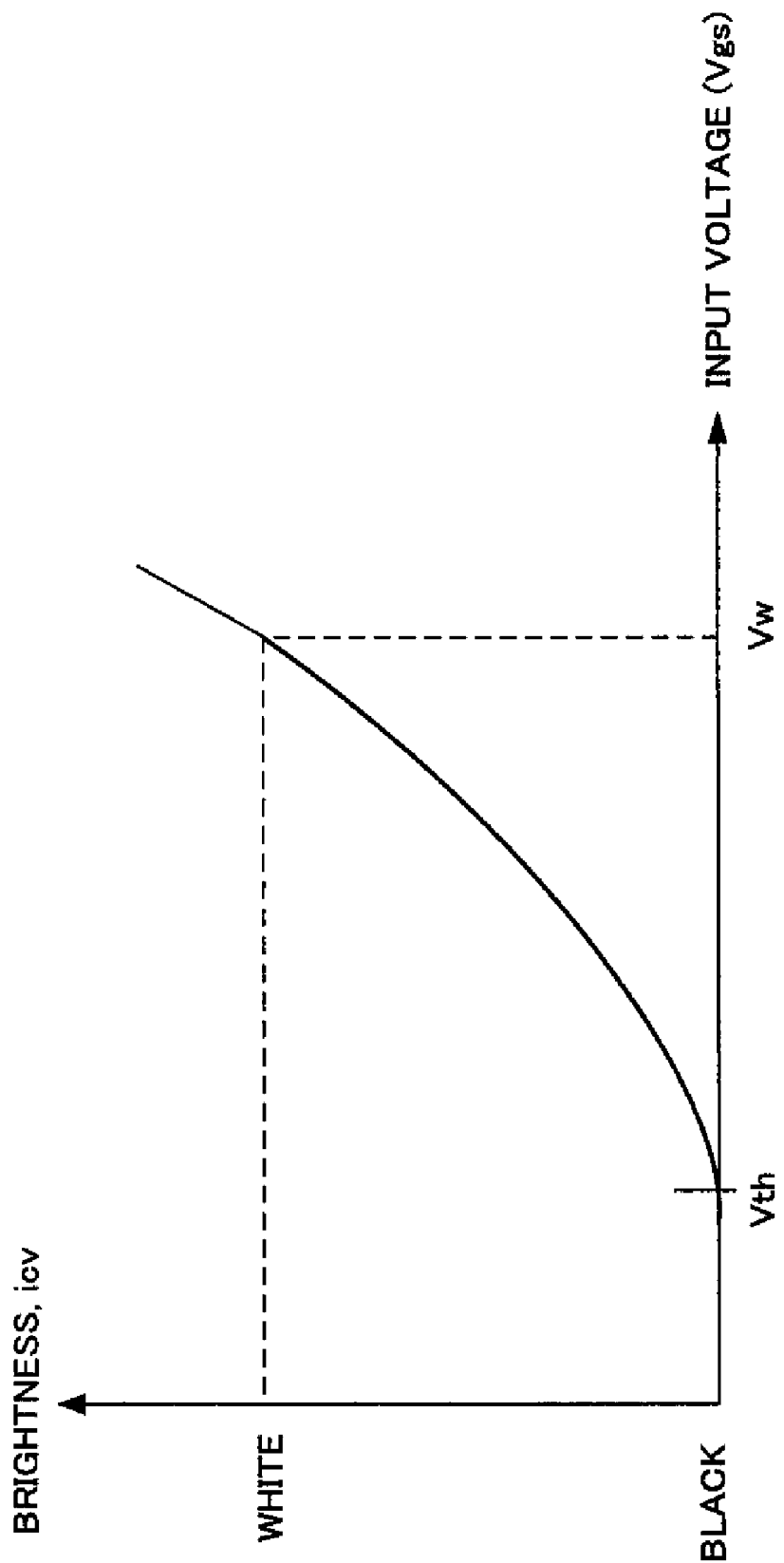


FIG. 2

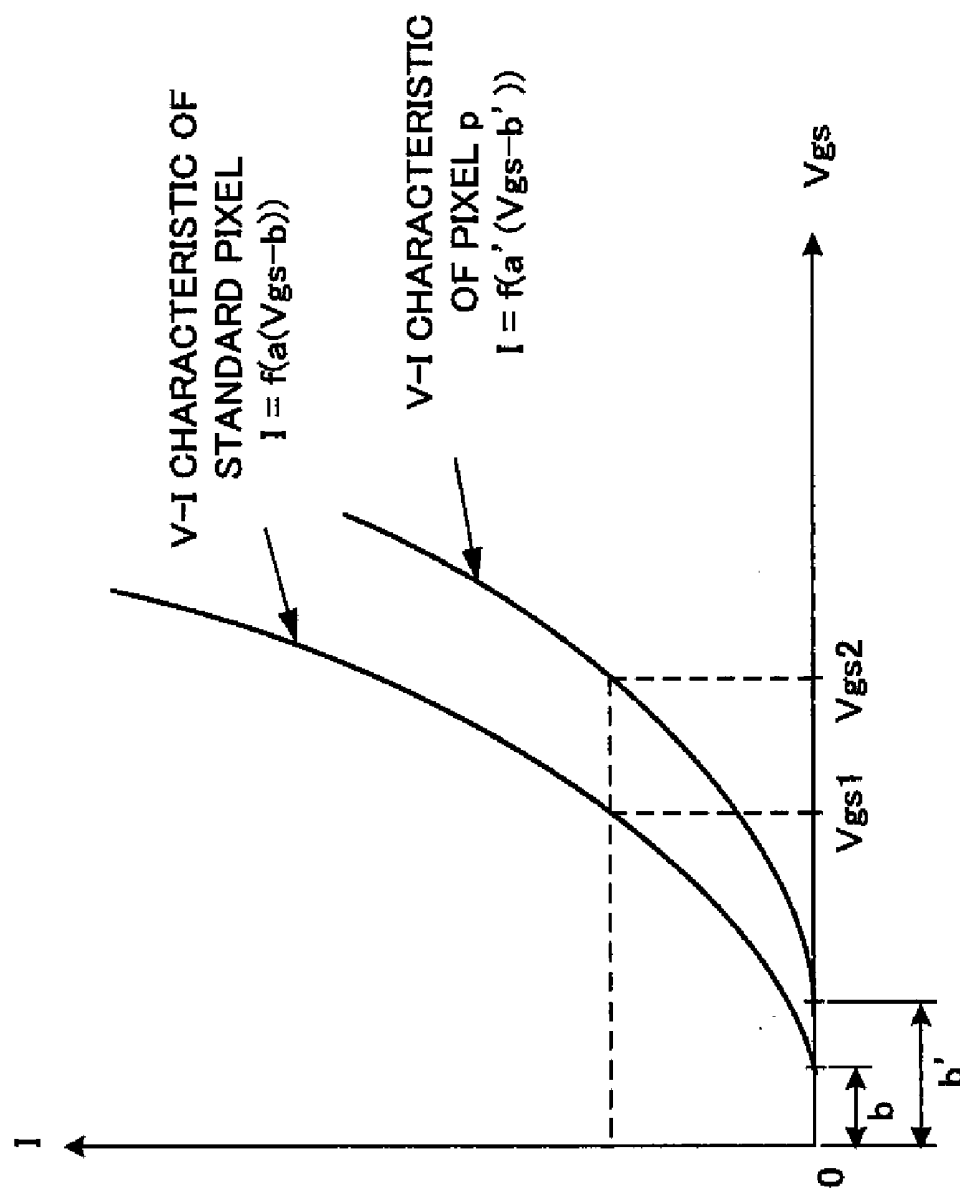


FIG. 3

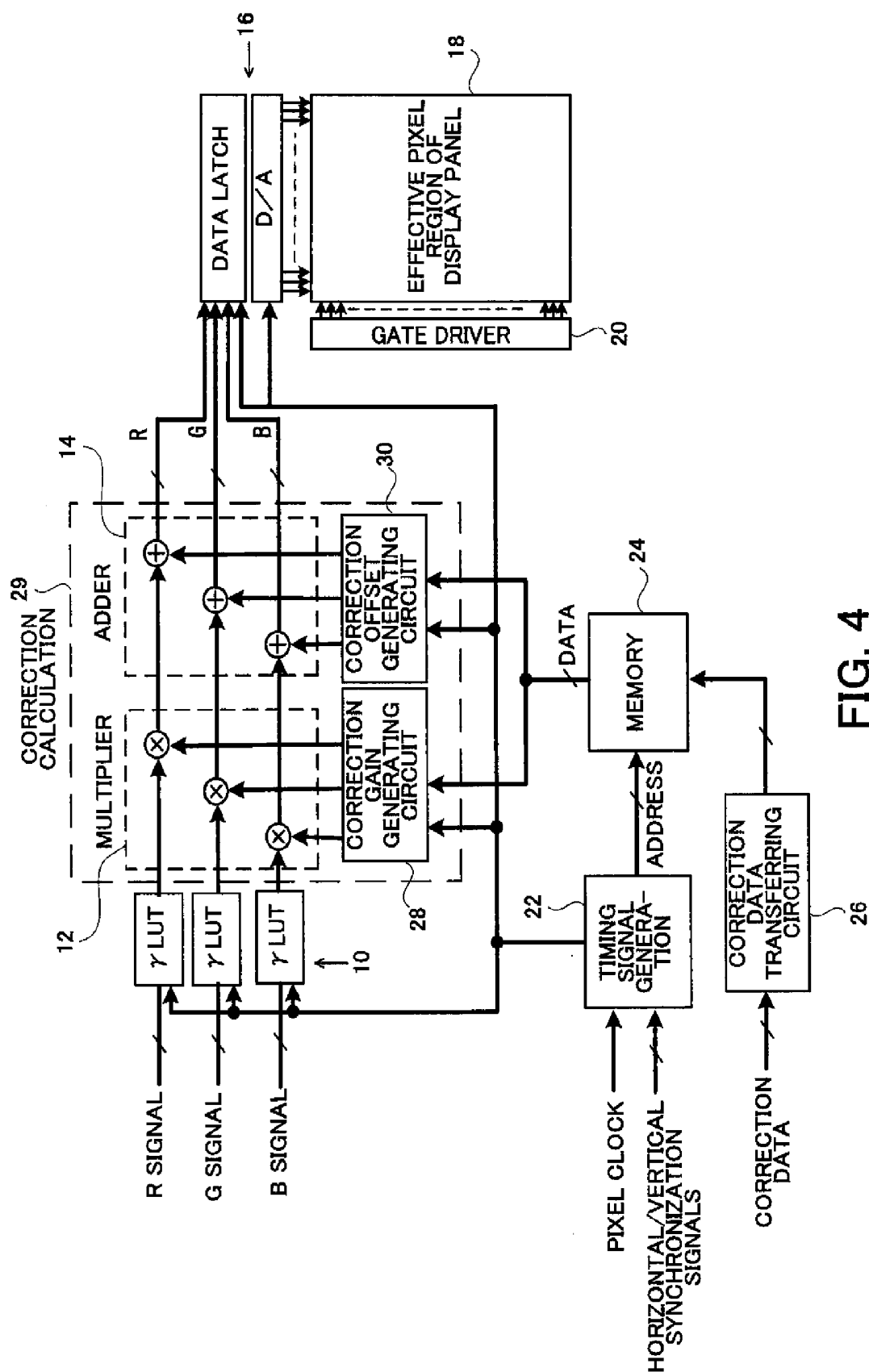


FIG. 4

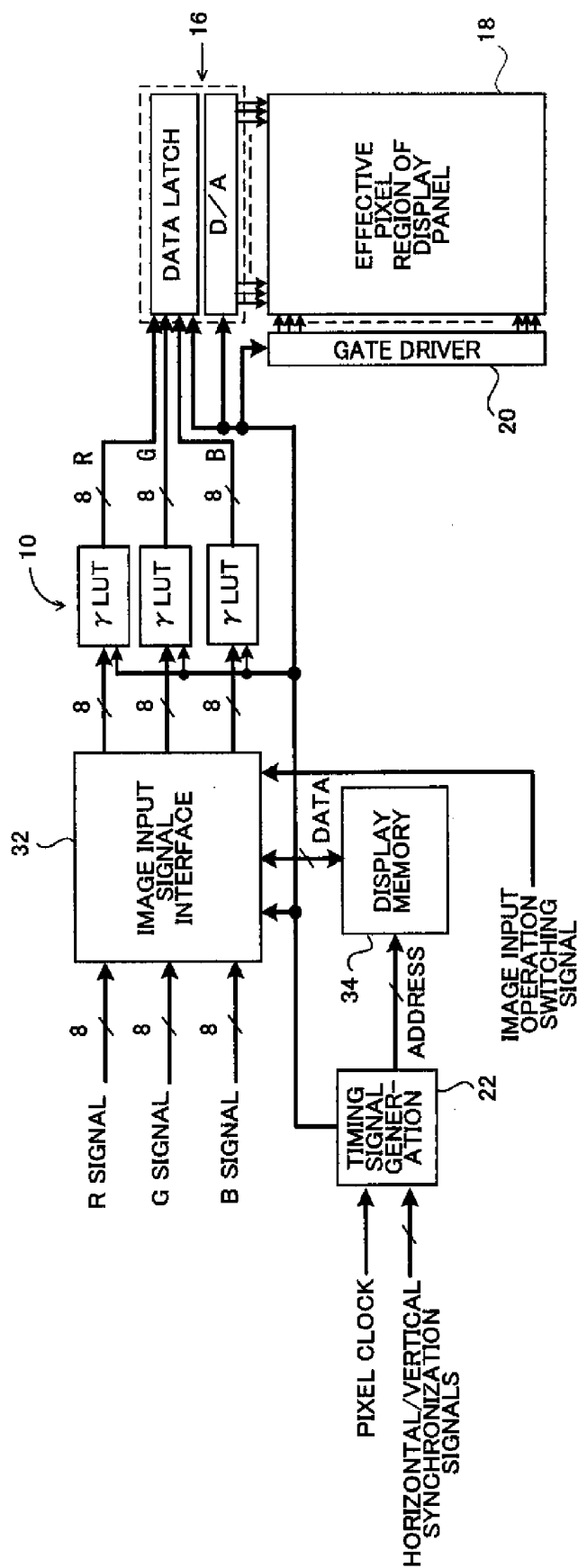


FIG. 5

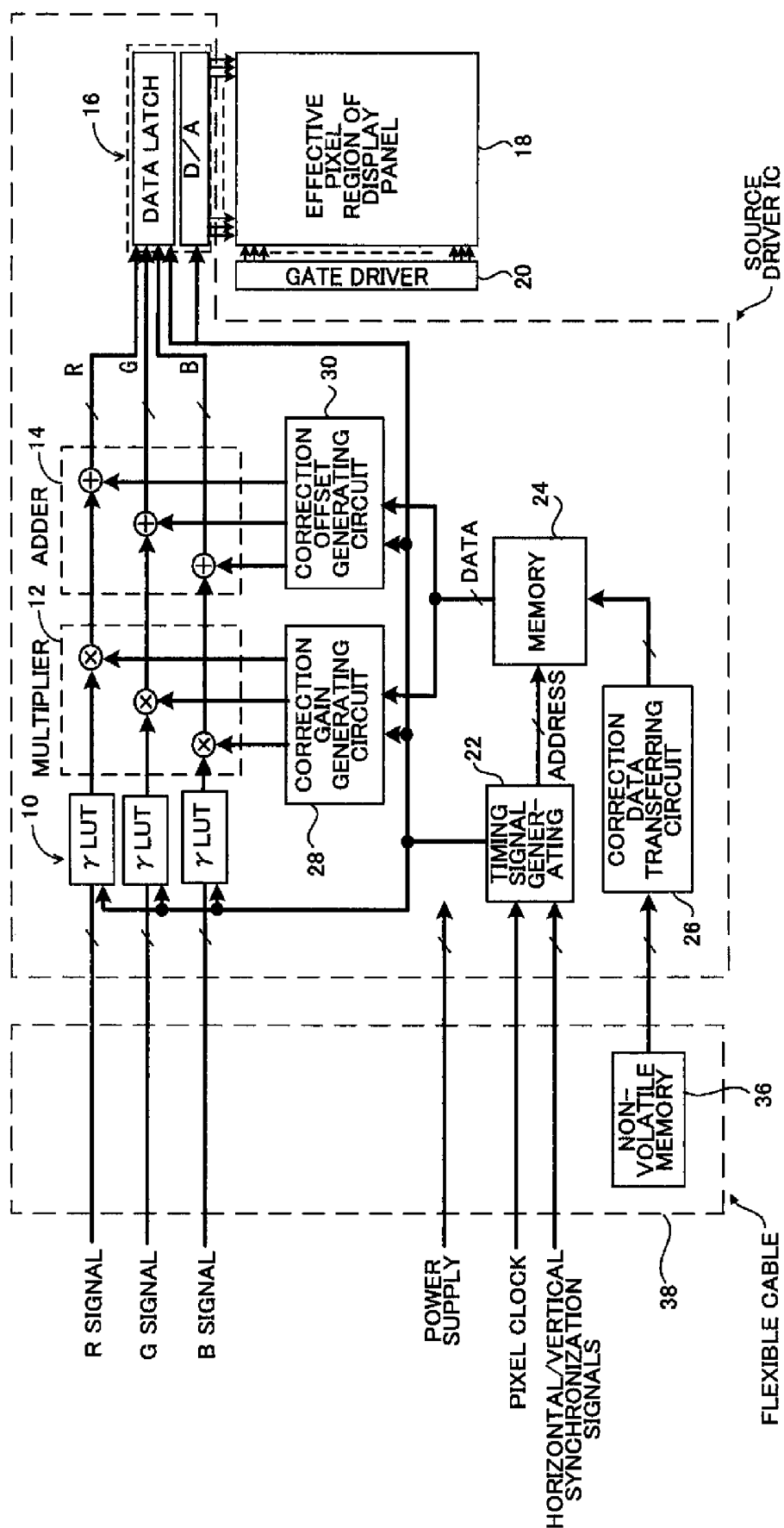


FIG. 6

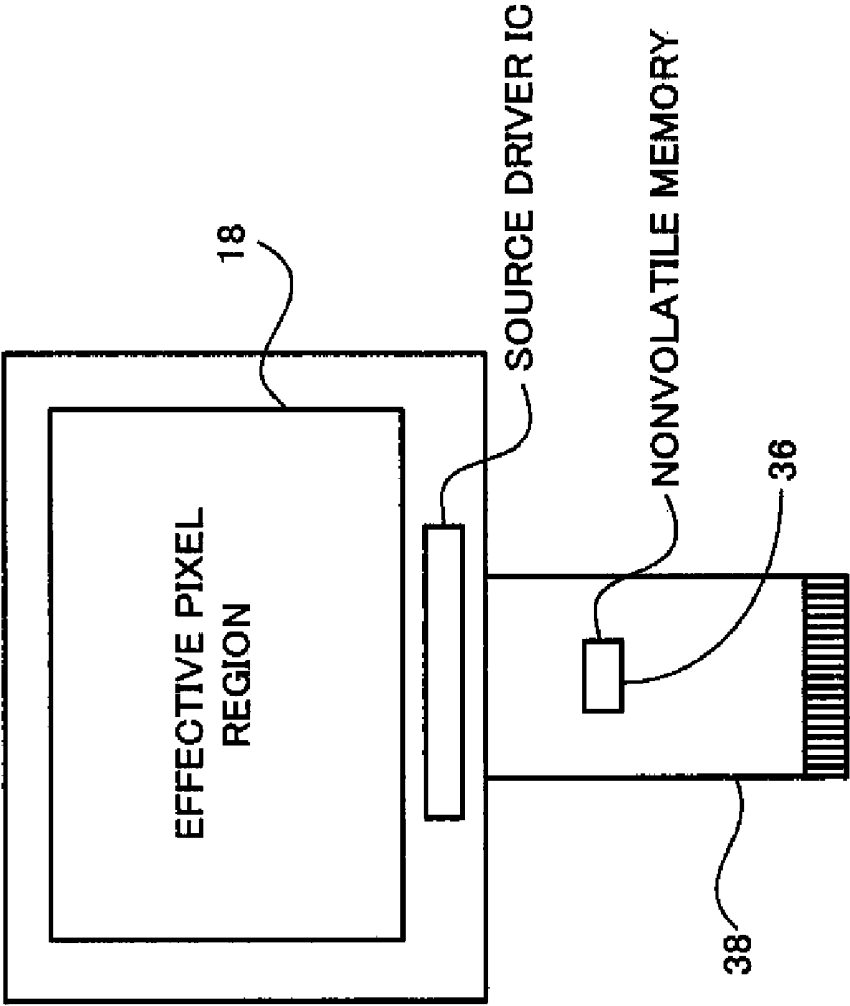


FIG. 7

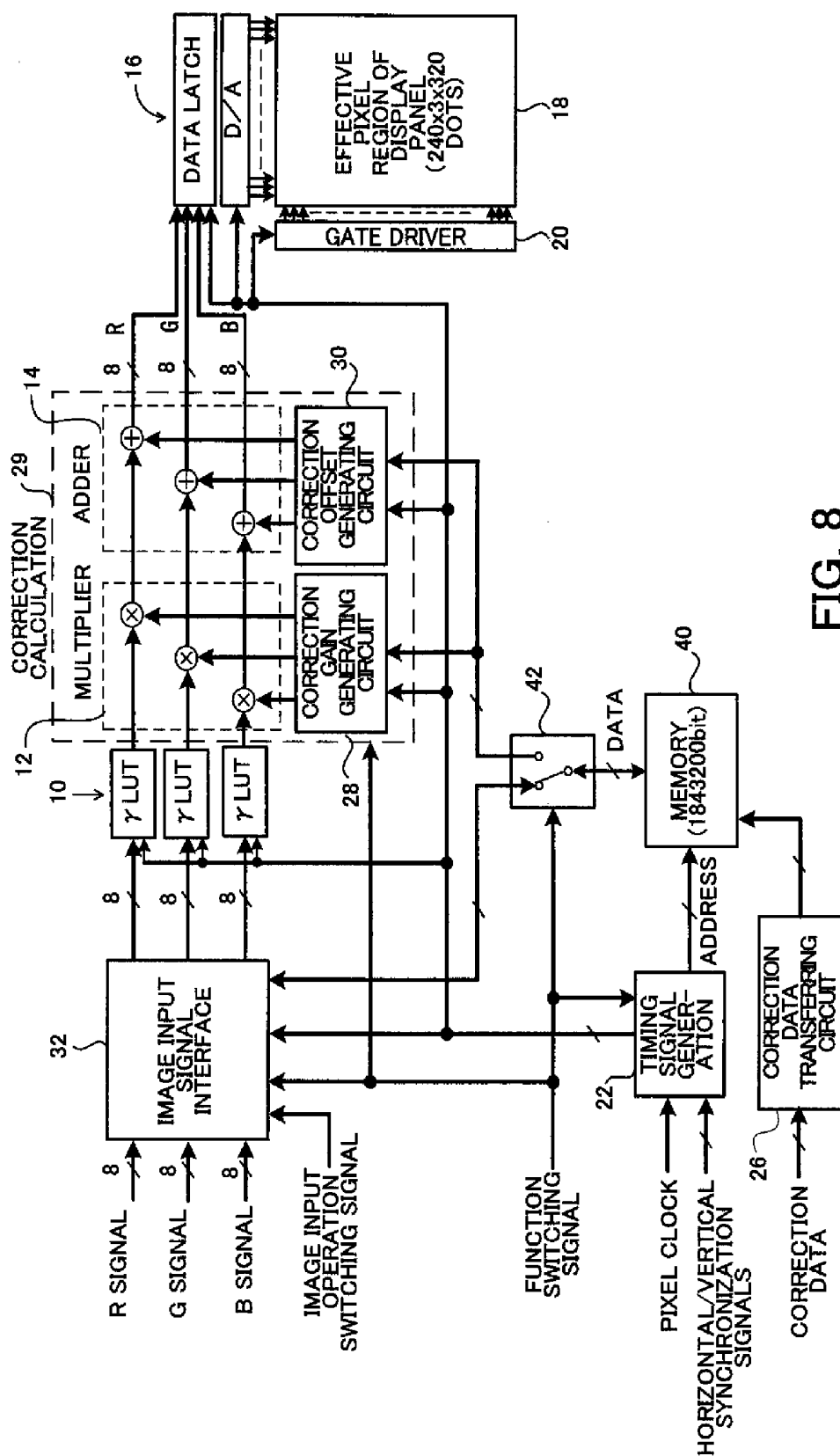


FIG. 8

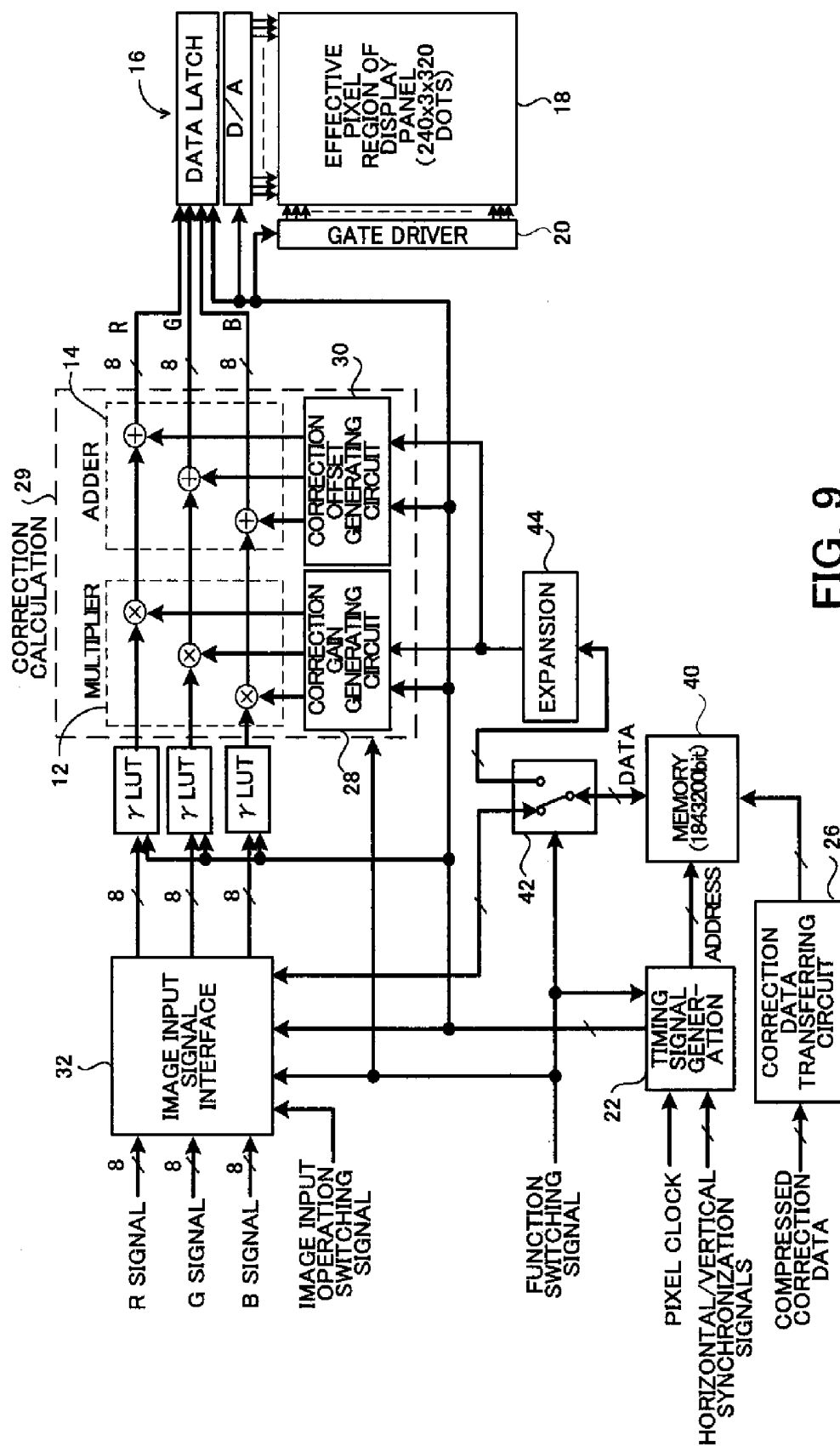


FIG. 9

CORRECTING BRIGHTNESS VARIATIONS IN ORGANIC ELECTROLUMINESCENT PANEL

CROSS-REFERENCE TO RELATED APPLICATION

[0001] This application claims priority of Japanese Patent Application No. 2008-068632 filed Mar. 18, 2008 which is incorporated herein by reference in its entirety.

FIELD OF THE INVENTION

[0002] The present invention relates to a driver IC for a display, and an organic electroluminescence ("EL") panel which uses the driver IC.

BACKGROUND OF THE INVENTION

[0003] Conventionally, an organic EL display is known. The organic EL display includes a driving thin film transistor ("TFT") for driving the organic EL element with a current, and in the organic EL display, an amount of current of the driving TFT is controlled according to image data.

[0004] Due to reasons such as a problem in manufacturing or degradation with elapse of time, the V_{th} of the driving TFT or a slope (μ) of the V-I characteristic can vary, resulting in brightness unevenness. In order to correct the unevenness, there are cases in which a predetermined value is added to the image data for driving the pixels to correct V_{th} (offset correction) and in which the image data is multiplied by a predetermined value to correct μ (gain correction).

[0005] FIG. 3 shows a method of calculating correction data and FIG. 4 is a block diagram of a correction circuit. First, voltage-current characteristics of a number of pixels are measured in order to determine a curve of the V-I characteristic of standard pixels in the panel. A function $f(x)$ is determined assuming that this curve is a curve represented by an equation of $I=f(a(V-b))$. Assuming that a total of pixels of the panel is represented by $f(x)$ and the variation in the characteristics is due to differences in the coefficients a and b , it is possible to determine the coefficients a and b of the pixels by, for example, measuring pixel currents corresponding to two or more input voltage levels.

[0006] When the V-I characteristic of a pixel p is represented by $I=f(a'(V-b'))$, it is possible to determine, based on the coefficients a and b for an average pixel which are already determined, $offset=k(b'-ab/a')$ and $gain=a/a'$ with the coefficient k being a coefficient of the D/A conversion, and correction can be executed by multiplying the image data by the determined gain and adding the determined offset.

[0007] More specifically, as shown in FIG. 4, for each of an R signal, a G signal, and a B signal which are image data, γ -corrected image data is obtained in a γ lookup table (γ LUT) 10 for obtaining a linear relationship between input pixel data and pixel current. The γ -corrected image data is multiplied by the correction gain, gain, in a multiplier 12 and a correction offset, offset, is added in an adder 14.

[0008] An image signal (R, G, B) for which unevenness is corrected is supplied to a display panel 18 through a data driver 16 having a data latch and a D/A converter, and is displayed in the display panel 18. A gate driver 20 is connected to the display panel 18, and the gate driver 20 controls to which line of the display panel 18 the image data is supplied.

[0009] A timing signal generating unit 22 generates various timing signals based on a pixel clock, a horizontal synchronization signal, and a vertical synchronization signal, and also generates an address of a memory 24. The memory 24 includes a RAM which can be read and written quickly, and, when the power supply is started up, correction data (gain, offset) are sent from an external nonvolatile memory or the like through a correction data transferring circuit 26, and is stored in the memory 24. The timing signal generating unit 22 generates, in correspondence with the image data of each pixel, an address at which the correction data for that pixel is stored, the correction data for each pixel is read from the memory 24, and the correction data are supplied to the multiplier 12 and the adder 14 through a correction gain generating circuit 28 and a correction offset generating circuit 30. The correction gain generating circuit 28, the correction offset generating circuit 30, the multiplier 12, and the adder 14 form a correction calculating unit 29.

[0010] In this manner, the unevenness can be significantly improved by calculating the γ -corrected signal data. Such an unevenness correction is described in, for example, JP 11-282420; U.S. Pat. No. 7,345,660, U.S. Patent Application Publication No. 2007/273701 and WO 2005/101360.

[0011] Some driver ICs for mobile devices have a built-in display RAM (display memory) which is called a graphic RAM, and do not require, once a static image is written to the display memory, transfer of the image signal from the outside unless the display image is to be changed. In FIG. 5, an image input signal interface 32 has functions to temporarily store an input image signal having 8 bits for each color in a display memory 34 and to send the stored image to the γ LUT 10. In addition, for a signal which is continuously input in synchronization to the pixel clock, the signal can be sent to the γ LUT 10 without any processing. The switching between these operations is achieved by a switching signal from a CPU or the like on the side of the system. In addition, in general, graphic functions are provided in which a line or a drawing is written on an image on the display memory 34, an image is scrolled, or an image is enlarged or reduced.

[0012] In a driver IC having such a display memory 34, although there are advantages that the unnecessary radiation from the data bus between the system-side circuit and the driver IC can be reduced, the power consumption by the data transfer can be reduced, and the load of the system-side circuit can be reduced, there also is a disadvantage in that the chip size is increased because of the large-capacity memory, and consequently the cost is increased. Currently, in general, a display memory is equipped in many cases in a panel for a portable phone having more frequent occurrences of occasions to display a still image, and the display memory is not equipped in a built-in panel for a monitor of a digital camera and a video camera which frequently displays an animated image also. Therefore, the determination of whether or not the display RAM is to be equipped cannot be generally made, and is currently comprehensively made according to the application.

[0013] When all pixels are to be corrected using the unevenness correction circuit, the correction data is required for each pixel, and consequently a memory for storing the data for the number of pixels of the panel is required. Currently, as shown in FIGS. 6 and 7, correction is executed by writing data of the unevenness in an external nonvolatile memory 36 at the time of shipping of the panel, reading all correction data to the memory (RAM) 24 in the driver IC

when the power supply of the panel module is started up, and correcting with the use of the data in the memory 24. In this example configuration, the nonvolatile memory 36 is mounted on a flexible cable 38.

[0014] In the case of a display panel 18 with a large number of pixels, a large-capacity RAM would be required as the memory 24, which affects the chip size of the driver IC and also increases the cost. As described, the occupied percentage of the correction memory 24 in the size of the driver IC is significant, and for a panel having a smaller number of occurrences of the unevenness because of optimization of the TFT manufacturing process or devising of the pixel circuit, it is advantageous, in view of the cost, to not equip the circuit for unevenness correction using an external circuit in the panel.

[0015] On the other hand, some driver ICs for mobile devices are equipped with a display memory 34 which is a display RAM, and in this case also, the memory for the number of pixels of the panel is used. However, as described above, there are some applications that do not require the display memory.

[0016] In other words, in an ideal structure, for a panel having the same number of pixels and the same driving method, four types of driver ICs are provided including a driver IC having neither the unevenness correction function nor the display memory function, a driver IC having only the unevenness correction function, a driver IC having only the display memory function, and a driver IC having both the unevenness correction function and the display memory function, and the driver ICs are used differently according to the usage. However, in reality, it is difficult to develop four types of driver ICs in view of the development cost and the number of steps for development, and furthermore, because of the variation of the types of the components, cost reduction by mass production cannot be expected.

SUMMARY OF THE INVENTION

[0017] According to one aspect of the present invention, there is provided a driver IC for a display comprising an unevenness correcting unit which executes a calculation based on image data which is input and correction data for correcting variation in brightness among pixels and corrects the brightness unevenness, a display memory unit which outputs stored image data when a same image is displayed, and a memory which can selectively store correction data which is used in the unevenness correcting unit or image data which is used in the display memory unit, wherein it is possible to select which of the unevenness correcting unit and the display memory unit uses the memory.

[0018] According to another aspect of the present invention, it is preferable that the driver IC is applied to an organic electroluminescence panel.

[0019] According to various aspects of the present invention, it is possible to select whether the memory is used for unevenness correction or as a display memory. Because of this, a driver IC can be provided in which the chip size is not significantly affected and the unevenness correction function and the display memory function can be selectively used according to the usage.

BRIEF DESCRIPTION OF THE DRAWINGS

[0020] Preferred embodiments of the present invention will be described in detail with reference to the drawings, wherein:

[0021] FIG. 1 is a diagram showing a structure of a pixel circuit;

[0022] FIG. 2 is a diagram showing a relationship between an input voltage and a current;

[0023] FIG. 3 is a diagram showing a difference in characteristics among pixels;

[0024] FIG. 4 is a diagram showing a structure for unevenness correction;

[0025] FIG. 5 is a diagram showing a structure when a display memory is used;

[0026] FIG. 6 is a diagram showing a structure in which a nonvolatile memory for storing correction data is provided;

[0027] FIG. 7 is a diagram showing a structure in which a nonvolatile memory for storing correction data is provided on a flexible cable;

[0028] FIG. 8 is a diagram showing a structure of a preferred embodiment of the present invention; and

[0029] FIG. 9 is a diagram showing another structure of a preferred embodiment of the present invention.

DETAILED DESCRIPTION OF THE INVENTION

[0030] A preferred embodiment of the present invention will now be described with reference to the drawings.

[0031] FIG. 8 is a block diagram of a preferred embodiment of the present invention. As shown, a driver IC includes a memory 40 which is selectively connected by a switch 42 to an image input signal interface 32 or a correction calculating unit 29.

[0032] In addition, all timing signals in the driver IC including the address of the memory 40 generated by the timing signal generating unit 22 are also switched according to the function. From the viewpoint of reduction of the power consumption, it is desirable that the clock and the timing pulse which are input to a circuit which is only used for one of the functions are stopped when the function is not being selected.

[0033] The switching of the functions is executed by, for example, connecting a function switching signal pin to a power supply or to a ground on a flexible cable during a production process. In addition, similar to settings such as the brightness and hue of the display, the setting can be realized by an output signal of a microcomputer or by storing a setting in the above-described external flash memory and reading the setting at the startup of the power supply.

[0034] When a function switching signal is set, the switch 42 connects the memory 40 to one of the image input signal interface 32 and the correction calculating unit 29.

[0035] When the memory 40 is used as the display memory, the image input signal interface 32 is connected to the memory 40. When the same image is to be displayed, a CPU at the outside supplies, according to the status of the display, an instruction that the same image is to be displayed to the image input signal interface 32 with an image input operation switching signal. In this case, the image input signal interface 32 temporarily stores an input image signal in the display memory 34 and then sends the stored image to the γ LUT 10. With this process, the image signal in the memory 40 is repeatedly displayed on the display panel 18. In addition, it is also possible to realize graphic functions, with an instruction from the CPU, to write a line or a drawing on an image on the display memory 34, to scroll the image, and to enlarge or reduce the image.

[0036] In the case of display of an animated image, on the other hand, signals which are continuously input in synchronization to the pixel clock are sent to the γ LUT 10 without any

processing. In this manner, a display similar to the normal display is realized on the display panel 18.

[0037] When the memory 40 is used as the display memory for correction data, the correction calculating unit 29 is connected to the memory 40. During startup of the power supply or the like, correction data stored in the external nonvolatile memory is written by the correction data transferring circuit 26 to the memory 40, and the correction data is supplied to the correction calculating unit 29.

[0038] A size of the memory 40 when the memory 40 is used as a display memory is, for example, for a panel of QVGA size (240 RGB×320) with a sub-pixel of 8 bits, 240×3×320×8=1843200 bits.

[0039] On the other hand, when the memory 40 is used as the memory for unevenness correction, 8 bits can be assigned for 1 dot. In this case, when only the threshold voltage V_{th} of the driving TFT is to be corrected, that is, only the offset is to be corrected, 8 bits can be assigned for the offset correction. When, on the other hand, μ , that is, gain, is also corrected, 4 bits can be assigned to each of the offset and gain corrections. [0040] With 4 bits, however, there can be cases where a dense unevenness cannot be sufficiently corrected, and it is thus desirable to compress data as described in U.S. Patent Application Publication No. 2007/273701. When the data compression of U.S. Patent Application Publication No. 2007/273701 is used, the density of the unevenness which can be corrected is not limited by the size of the memory 40, and a total amount of unevenness over the whole panel which can be corrected is limited by the size of the memory 40. Normally, even when both the offset and the gain are corrected, the above-described memory size is sufficient unless there is a dense unevenness over the entire display. FIG. 9 is a block diagram of an example structure in this case.

[0041] The compressed correction data is written to the memory 40 through the correction data transferring circuit 26. An expansion circuit 44 is provided between the switch 42 and the correction gain generating circuit 28 and the correction offset generating circuit 30. Therefore, when the memory 40 is used for unevenness correction, the compressed correction data is stored in the memory 40 and is expanded by the expansion circuit 44, and is used for correction.

[0042] In the present embodiment, the storage unit to be used for unevenness correction and the storage unit to be used for display memory function are combined into the memory 40, and the unevenness correction function and the display memory function are set to be selectable with one driver IC. With this structure, it is possible to realize a driver IC having only the unevenness correction function or only the display memory function with one type of driver IC. In this case, when the unevenness correction function is selected, the logic unit which is used for the display memory function is not used and is redundant, and when the display memory function is selected, on the other hand, the logic unit which is used for the unevenness correction function is not used and is redundant. However, the occupied ratio of these logic units within the chip size is very small compared to that of the RAM and the D/A converter, and the provision of these units does not significantly affect the chip size of the IC. In general, with the sharing of the driver IC, the advantages of the reduction of the cost by the reduction in the development cost and advantages of scales is greater than the above-described disadvantage.

[0043] The invention has been described in detail with particular reference to certain preferred embodiments thereof, but it will be understood that variations and modifications can be effected within the spirit and scope of the invention.

Parts List

[0044]	10 γ LUT
[0045]	12 multiplier
[0046]	14 adder
[0047]	18 display panel
[0048]	20 gate driver
[0049]	22 generating unit
[0050]	22 timing signal generating unit
[0051]	24 memory
[0052]	24 correction memory
[0053]	26 data transferring circuit
[0054]	28 gain generating circuit
[0055]	29 correction calculating unit
[0056]	30 offset generating circuit
[0057]	32 signal interface
[0058]	34 display memory
[0059]	36 nonvolatile memory
[0060]	38 flexible cable
[0061]	40 memory
[0062]	42 switch
[0063]	44 expansion circuit

1. An electroluminescence display, comprising:

- (a) an image input signal interface for receiving first image data;
- (b) a correction circuit for producing corrected image data in response to the first image data and in response to correction data to correct for brightness unevenness due to TFT variations;
- (c) a memory for storing first image data or correction data;
- (d) a switch effective in first and second states in response to a function switching signal having first and second conditions, respectively;
- (e) a panel for emitting light in response to second image data; and
- (f) means responsive to the function switching signal in its first condition for causing the switch to be in the first state to connect the memory to the image input signal interface and to provide the stored first image data to the panel as the second image data; and means responsive to the function switching signal in its second condition for causing the switch to be in the second state to connect the memory to the correction circuit, provide the stored correction data to the correction circuit, and provide the corrected image data to the panel as the second image data.

2. The electroluminescence display of claim 1, wherein the panel is an organic electroluminescence display panel.

3. The electroluminescence display of claim 1, wherein the stored correction data are compressed correction data, and further including an expansion circuit for receiving the compressed correction data and providing expanded correction data to the correction circuit.

* * * * *

专利名称(译)	校正有机电致发光面板的亮度变化		
公开(公告)号	US20090237334A1	公开(公告)日	2009-09-24
申请号	US12/401126	申请日	2009-03-10
[标]申请(专利权)人(译)	MIZUKOSHI SEIICHI 河野MAKOTO ONOMURA浩一		
申请(专利权)人(译)	MIZUKOSHI SEIICHI 河野MAKOTO ONOMURA浩一		
当前申请(专利权)人(译)	MIZUKOSHI SEIICHI 河野MAKOTO ONOMURA浩一		
[标]发明人	MIZUKOSHI SEIICHI KOHNO MAKOTO ONOMURA KOUICHI		
发明人	MIZUKOSHI, SEIICHI KOHNO, MAKOTO ONOMURA, KOUICHI		
IPC分类号	G09G3/30		
CPC分类号	G09G3/3233 G09G5/006 G09G2300/0819 G09G2310/04 G09G2360/18 G09G2320/0285 G09G2340/02 G09G2360/12 G09G2320/0233		
优先权	2008068632 2008-03-18 JP		
其他公开文献	US8149190		
外部链接	Espacenet USPTO		

摘要(译)

一种OLED显示器，具有校正电路，用于响应于第一图像数据产生校正图像数据并响应校正数据以校正由于TFT变化引起的亮度不均匀；存储器，用于存储第一图像数据或校正数据；响应于具有第一和第二条件的功能切换信号，分别在第一和第二状态有效的开关；以及用于使开关处于第一状态以将存储器连接到图像输入信号接口并将存储的第一图像数据作为第二图像数据提供给面板的电路；并且，为了使开关处于第二状态以将存储器连接到校正电路，将存储的校正数据提供给校正电路，并将校正的图像数据作为第二图像数据提供给面板。

